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SERIAL NUMBER 10/712,455	FILING DATE 11/12/2003 RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. TI-35362
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APPLICANTS

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** CONTINUING DATA *****

This appln claims benefit of 60/428,821 11/25/2002

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/23/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	TX	1	10	3
Verified and Acknowledged	 Examiner's Signature	Initials			

ADDRESS

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TITLE

Method to manufacture LDMOS transistors with improved threshold voltage control

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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No. _____ for following:

1.18 Fees (Issue)

Other _____

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